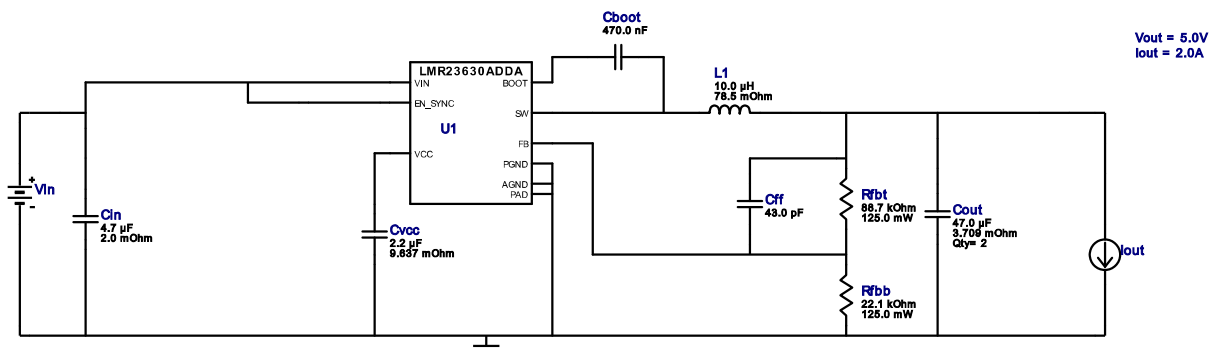


WEBENCH® Design Report

Design : 4774910/2 LMR23630ADDAR
LMR23630ADDAR 14.0V-22.0V to 5.00V @ 2.0A



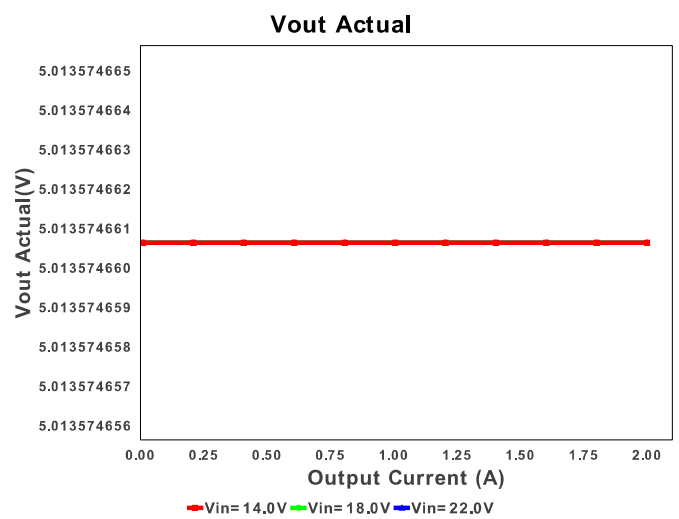
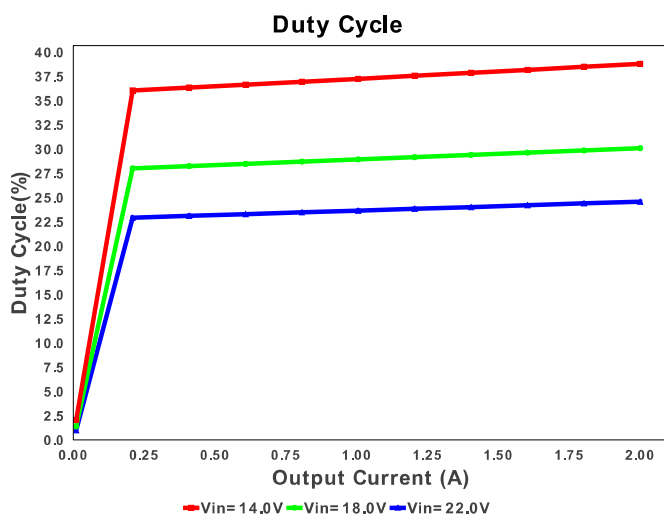
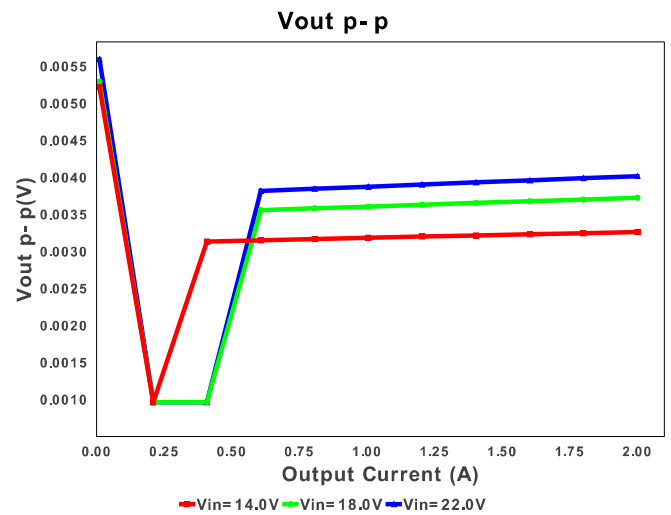
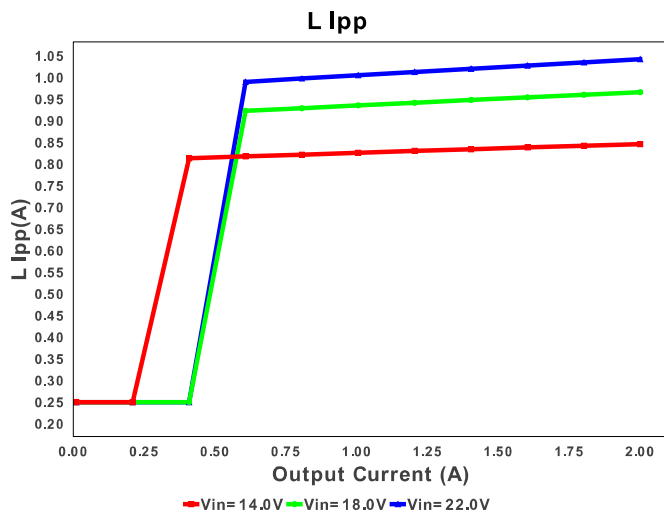
1. The input capacitor included in the BOM only contains a small filter capacitor that should be placed near the IC. Depending on where the power supply is laid out in the system additional bulk capacitance may need to be added to filter the line ripple.

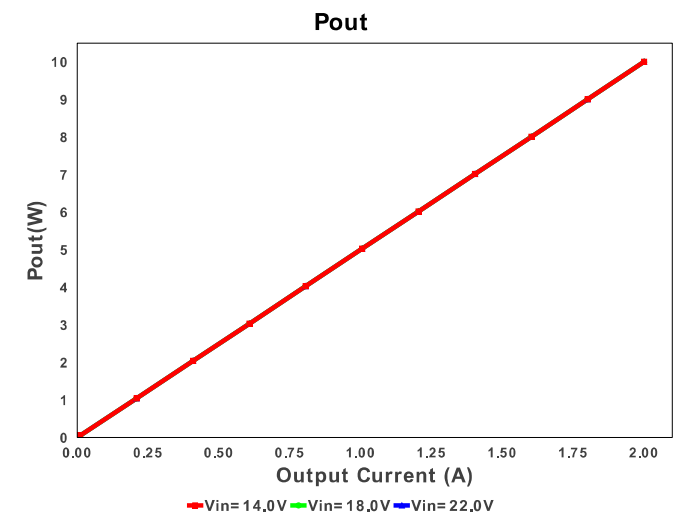
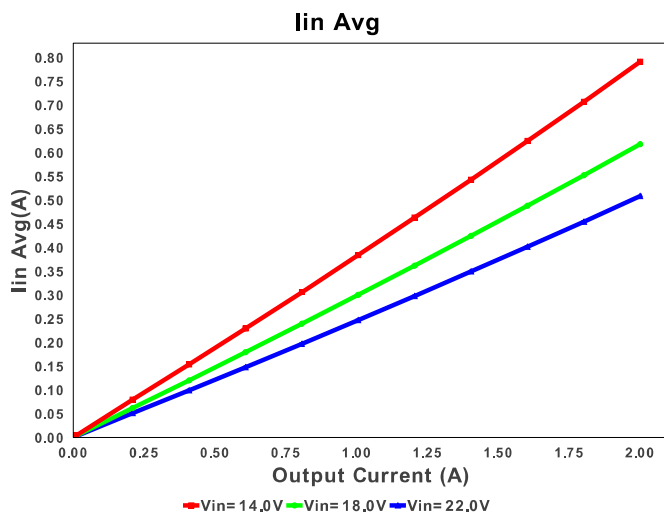
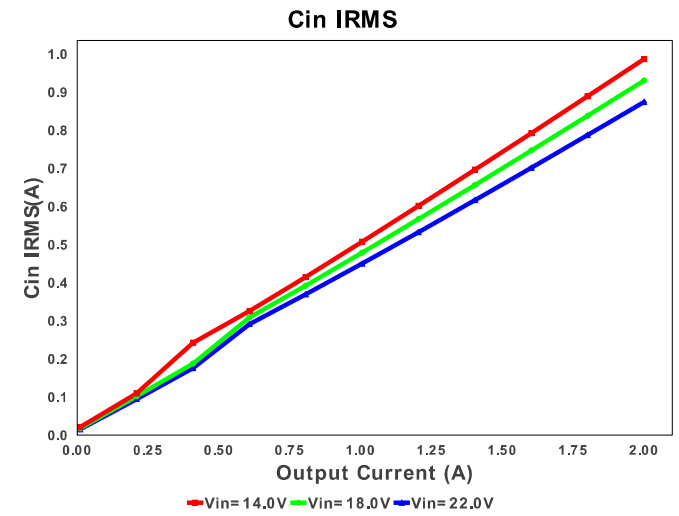
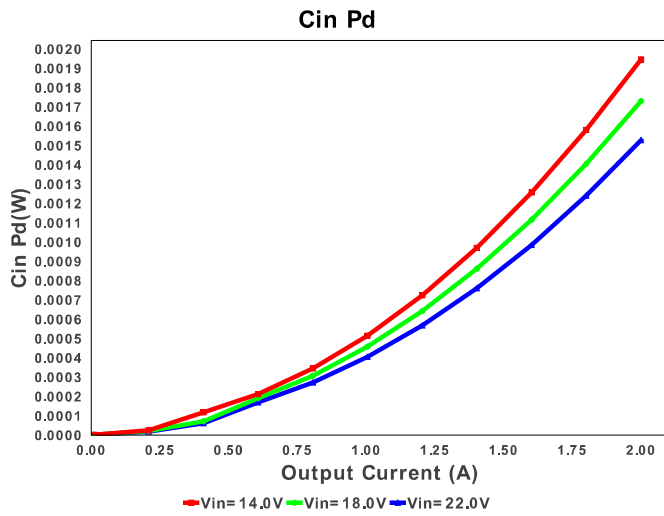
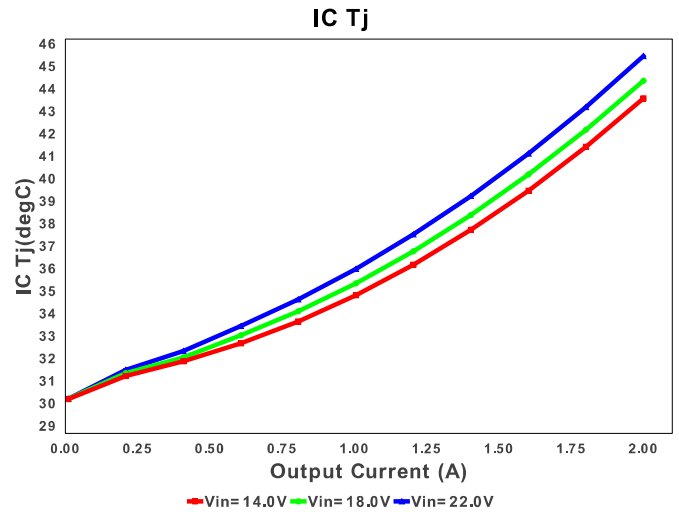
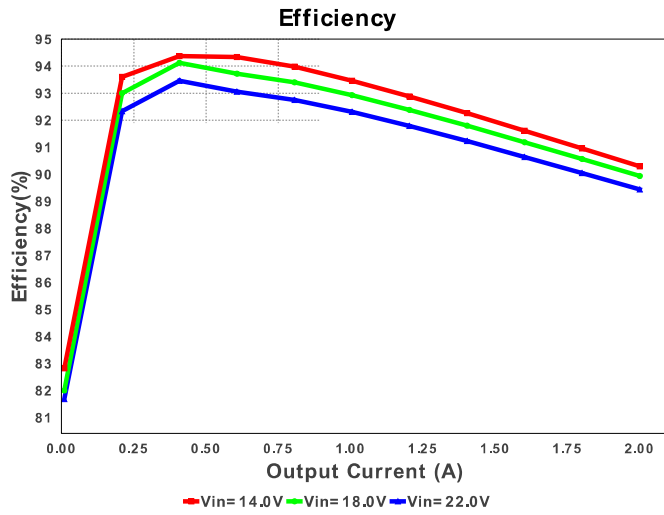
2. If there is no VinTyp specified, WEBENCH will use the VinMax value. To change the VinTyp value, click on the "Change Design Inputs" button under the Optimization Tuning knob. In some applications, while the design requires the input voltage to be a wide range, for a majority of the time, it is operating at a much lower voltage than the maximum input voltage. Sizing the inductor based on the maximum input voltage may yield an inductance much larger than typically needed, causing a larger footprint for the overall design. At the same time, components such as the input capacitor must be rated based on the maximum input voltage. WEBENCH now supports the use of this additional input voltage specification.

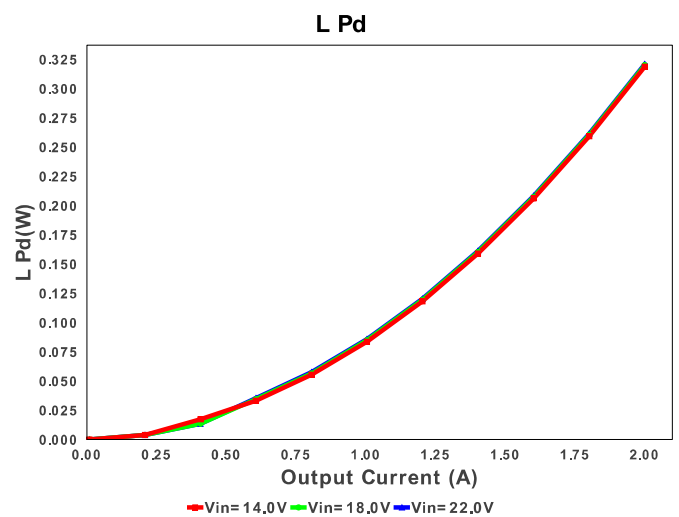
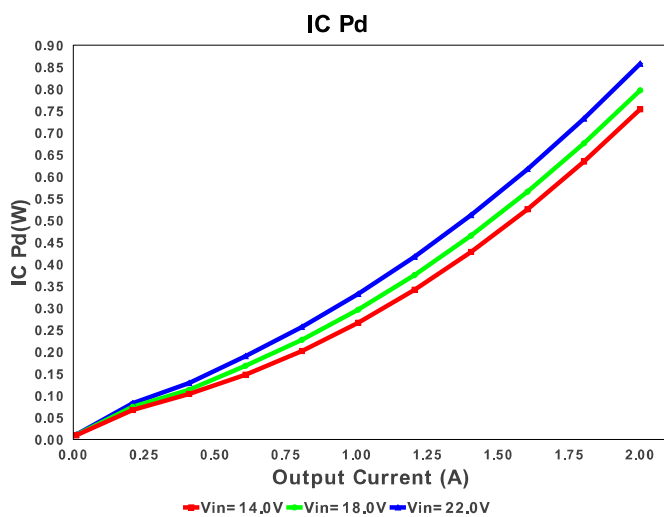
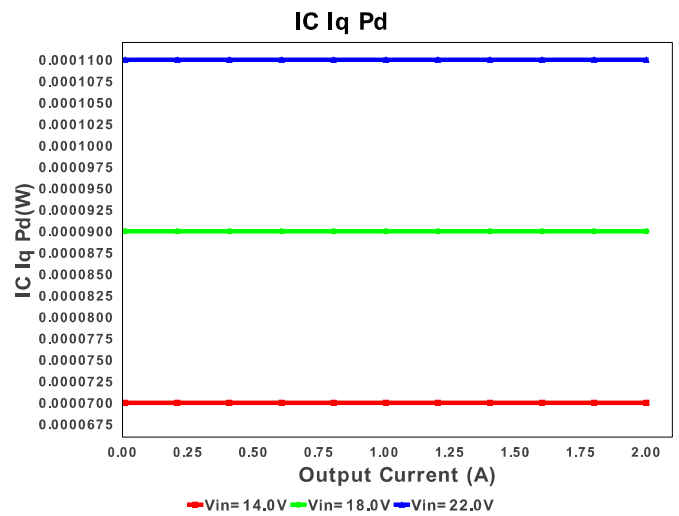
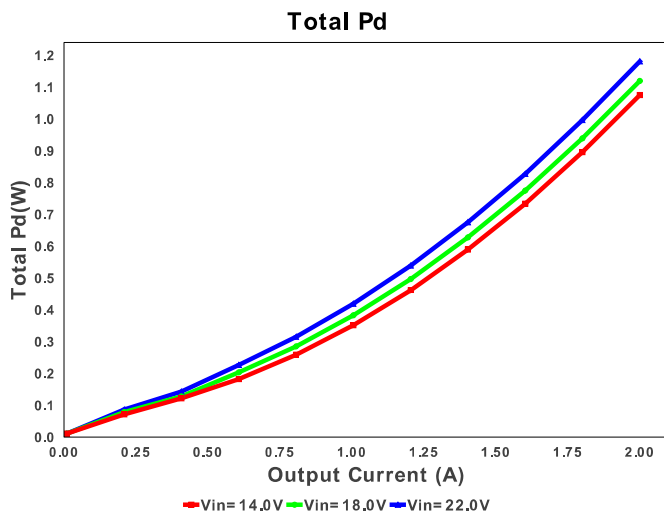
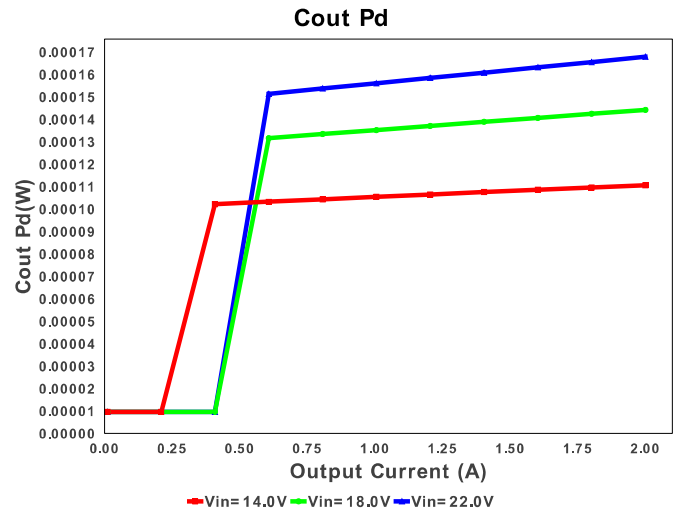
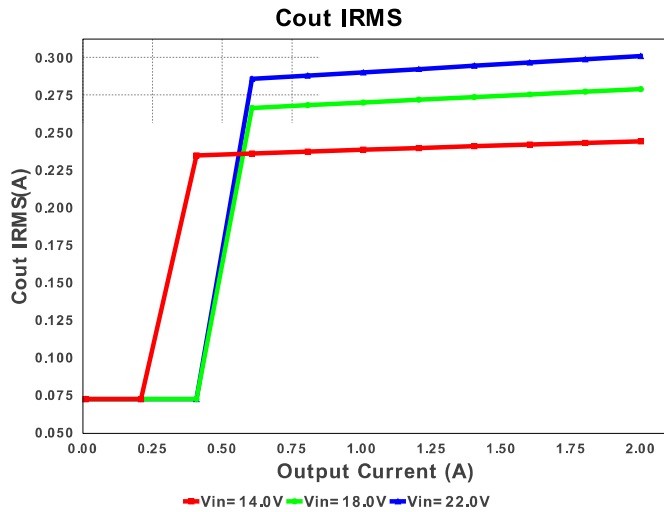
Electrical BOM

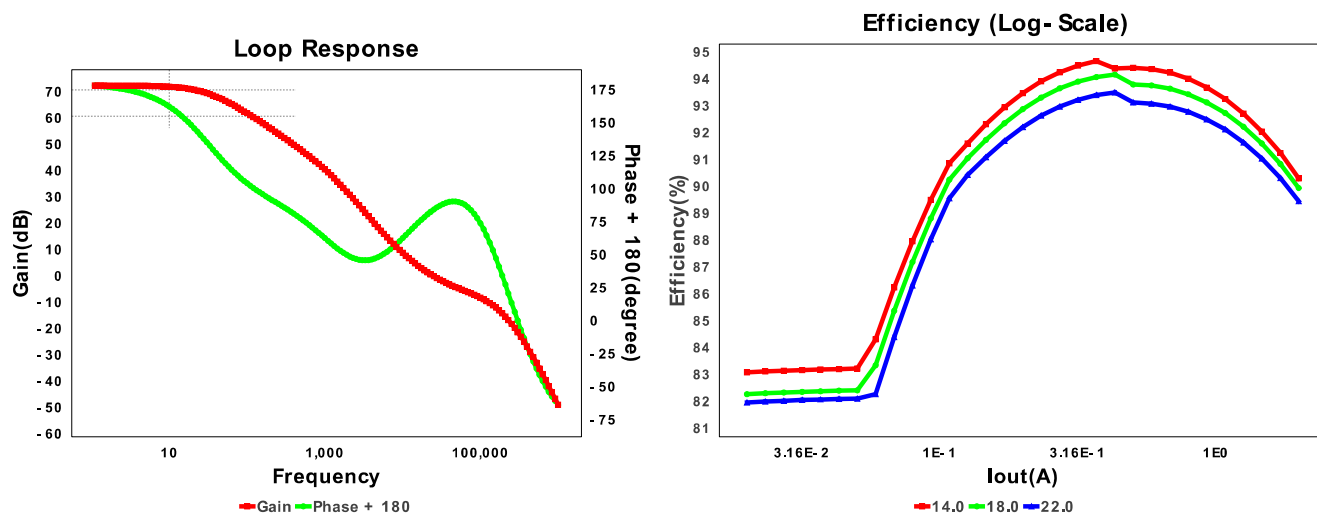
#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cboot	Taiyo Yuden	GMK212BJ474KG-T Series= X5R	Cap= 470.0 nF VDC= 35.0 V IRMS= 0.0 A	1	\$0.03	 0805 7 mm ²
2.	Cff	Samsung Electro-Mechanics	CL21C430JBANNNC Series= C0G/NP0	Cap= 43.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	 0805 7 mm ²
3.	Cin	MuRata	GRM32ER71H475KA88L Series= X7R	Cap= 4.7 uF ESR= 2.0 mOhm VDC= 50.0 V IRMS= 5.35 A	1	\$0.19	 1210 15 mm ²
4.	Cout	MuRata	GRM31CR61A476KE15L Series= X5R	Cap= 47.0 uF ESR= 3.709 mOhm VDC= 10.0 V IRMS= 4.2862 A	2	\$0.15	 1206_190 11 mm ²
5.	Cvcc	MuRata	GRM188R60J225KE19D Series= X5R	Cap= 2.2 uF ESR= 9.637 mOhm VDC= 6.3 V IRMS= 1.32271 A	1	\$0.02	 0603 5 mm ²
6.	L1	Bourns	SRP6540-100M	L= 10.0 uH DCR= 78.5 mOhm	1	\$0.49	 SRP6540 83 mm ²
7.	Rfbb	Panasonic	ERJ-6ENF2212V Series= ERJ-6E	Res= 22.1 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm ²
8.	Rfbb	Panasonic	ERJ-6ENF8872V Series= ERJ-6E	Res= 88.7 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm ²

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
9.	U1	Texas Instruments	LMR23630ADDAR	Switcher	1	\$1.54	 DDA0008E_N 57 mm ²









Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	873.941 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	300.85 mA	Current	Output capacitor RMS ripple current
3.	Iin Avg	508.22 mA	Current	Average input current
4.	L Ipp	1.042 A	Current	Peak-to-peak inductor ripple current
5.	BOM Count	10	General	Total Design BOM count
6.	FootPrint	208.0 mm ²	General	Total Foot Print Area of BOM components
7.	Frequency	400.0 kHz	General	Switching frequency
8.	Mode	CCM	General	Conduction Mode
9.	Pout	10.0 W	General	Total output power
10.	Total BOM	\$2.6	General	Total BOM Cost
11.	ICThetaJA Effective	18.0 degC/W	Op_Point	Effective IC Junction-to-Ambient Thermal Resistance
12.	Low Freq Gain	71.705 dB	Op_Point	Gain at 10Hz
13.	Vout Actual	5.014 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
14.	Vout OP	5.0 V	Op_Point	Operational Output Voltage
15.	Cross Freq	22.769 kHz	Op_point	Bode plot crossover frequency
16.	Duty Cycle	24.58 %	Op_point	Duty cycle
17.	Efficiency	89.437 %	Op_point	Steady state efficiency
18.	Gain Marg	-21.955 dB	Op_point	Bode Plot Gain Margin
19.	IC Tj	45.439 degC	Op_point	IC junction temperature
20.	IOUT_OP	2.0 A	Op_point	Iout operating point
21.	Phase Marg	82.157 deg	Op_point	Bode Plot Phase Margin
22.	VIN_OP	22.0 V	Op_point	Vin operating point
23.	Vout p-p	4.017 mV	Op_point	Peak-to-peak output ripple voltage
24.	Cin Pd	1.528 mW	Power	Input capacitor power dissipation
25.	Cout Pd	167.852 μW	Power	Output capacitor power dissipation
26.	IC Iq Pd	110.0 μW	Power	IC Iq Pd
27.	IC Pd	858.085 mW	Power	IC power dissipation
28.	L Pd	321.105 mW	Power	Inductor power dissipation
29.	Total Pd	1.181 W	Power	Total Power Dissipation
30.	Vout Tolerance	3.65 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	2.0	Maximum Output Current
2.	VinMax	22.0	Maximum input voltage
3.	VinMin	14.0	Minimum input voltage
4.	Vout	5.0	Output Voltage
5.	base_pn	LMR23630A	Texas Instruments Base Part Number
6.	source	DC	Input Source Type
7.	ta	30.0	Ambient temperature

Design Assistance

1. LMR23630A Product Folder : <http://www.ti.com/product/LMR23630> : contains the data sheet and other resources.

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